



PATENT

BUCKET NO.: P05062
CLIENT NO.: NATH15-05062
Customer No. 23990

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
RECEIVED

In re application of : HAL C. McCOWN
MAR 19 2003
U.S. Serial No. : 10/032,208
Technology Center 2100
Filed : October 26, 2001
For : SYSTEMS FOR SELECTIVELY DISABLING TIMING
VIOLATIONS IN HARDWARE DESCRIPTION LANGUAGE
MODELS OF INTEGRATED CIRCUITS AND METHODS OF
OPERATING THE SAME
Group No. : 2123
Examiner : Not yet assigned

Commissioner for Patents
Washington, D.C. 20231

Attn: Official Draftsman

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:

1. Postcard receipt;
2. Letter to Official Draftsman; and,
3. Two (2) Sheets of Formal Drawings.

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to: Commissioner for Patents, Washington, D.C. 20231, on March 10, 2003.

Date: March 10, 2003

Kathy Hamilton
Mailer

Date: March 10, 2003

William A. Munck
William A. Munck
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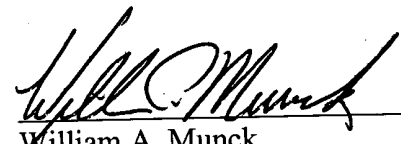
LETTER TO OFFICIAL DRAFTSMAN

Applicant submits two (2) sheets of formal drawings (FIGURES 1-2) in the above-referenced
patent application.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: March 10, 2003


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Registration No. 39,308

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